



In the validator, I see the input field for the password only appears once. I can't post the full page source because I'm not sure what to cut off. It's all public at Daring Fireball. A: You're doing things in a rather roundabout way. Elements all have IDs, and you're looking up an element by ID in the document. On a side note: your technique will break if you have any stray whitespace between your elements. Here's a way that ought to work.

```
var password = document.getElementById('password').value; var valid = document.getElementById('valid'); if (password == "qixr") { valid.classList.add('is-valid'); } else { valid.classList.remove('is-valid'); } console.log('password', password);
```

invalid password Notice how the password is verified as part of the input event listener. Also, the paragraph with the red word "invalid" is not updated unless the input fails a validator. And of course, you can use your password as the text for the paragraph instead of a red word. Then it just reads:

```
var password = document.getElementById('password').value; if (password == "qixr") { document.getElementById('valid').classList.add('is-valid'); } else { document.getElementById('valid').classList.remove('is-valid'); } console.log('password', password);
```

invalid password

Q: C++ using std::thread Error: No operator "=" matches these operands I was working with simple concurrent scheme. I would like to have 2 threads and pass objects between them. I wrote method with 2 arguments but I am getting compilation error. I'm not so experienced with threading I have no idea why it is happening.

```
void justTick(double value, std::string textureName) d0c515b9f4
```

Category Game, Game, Java., Serial Number Registry List. In current memory systems, memory subsystems may include multiple memory devices connected via one or more memory busses. One example of such a subsystem is a main memory subsystem. Typically, the main memory subsystem comprises one or more memory devices connected to the memory controller via a memory bus. To improve memory performance, the number of memory devices that are grouped into a memory subsystem may be increased. Increasing the number of memory devices in a memory subsystem may thus provide higher memory performance. In some memory systems, memory devices within the memory subsystem may operate in response to a clock signal (e.g., within a particular clock domain). The memory devices may have a clock tree for distributing the clock signal to various modules within the memory devices. The clock tree may route the clock signal throughout the memory devices within the memory subsystem. In certain memory systems, it is desirable to distribute the clock signal to memory devices in response to the clock signal without interfering with data being read from the memory devices. An example of a memory device may be a DRAM (Dynamic Random Access Memory). A DRAM may comprise circuitry for receiving a control signal and/or a clock signal to control operation of a DRAM. The control signal and/or the clock signal may be a rise time and/or a fall time signal. A rise time is the amount of time it takes a signal to go from zero to one. A fall time is the amount of time it takes a signal to go from one to zero. The control signal and/or the clock signal may be transmitted to the memory device via a memory bus. Data, Data, Everywhere - henrik\_w ===== hackerboos The main reason why a lot of people are excited about the Internet of Things is that they are using the sensor data to make things happen. Sensor Based Surveillance: [ th...]

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